

**INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION**

ATTY. DOCKET NO.
43876-128

SERIAL NO.
09/922,319

APPLICANT
HANSEN, et al.

(PTO-1449)

FILING DATE
August 02, 2001

GROUP
2671

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
MM	4,025,772	05/24/77	Constant			
	4,489,393	12/18/84	Kawahara, et al.			
	4,701,875	10/20/87	Konishi, et al.			
	4,727,505	02/23/88	Konishi, et al.			
	4,876,660	10/24/89	Owens, et al.			
	4,893,267	01/09/90	Alsup, et al.			
	4,956,801	09/11/90	Priem et al.			
	4,969,118	11/06/90	Montoye, et al.			
	4,975,868	12/04/90	Freerksen			
	5,032,865	07/16/91	Schlunt			
	5,157,388	10/20/92	Kohn			
	5,201,056	04/06/93	Daniel, et al.			
	5,268,855	12/07/93	Mason, et al.			
MM	5,268,995	12/07/93	Diefendorff, et al.			

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EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

MM	Parallel Computers for Graphics Applications, Adam Levinthal, Pat Hanrahan, Mike Paquette, Jim Lawson, Pixar San Rafael, California, 1987
MM	Organization of the Motorola 88110 Superscalar RISC Microprocessor, Keith Diefendorff and Michael Allen.
MM	Microprocessor Report, Volume 7 Number 13, October 4, 1993, IBM Regains Performance Lead with Power2, Six Way Superscalar CPU in MCM Achieves 126 SPECint92.
MM	IBM Creates PowerPC Processors for AS/400, Two New CPU's Implement 64-Bit Power PC with Extensions by Linley Gwennap, July 31, 1995.

EXAMINER <i>Dickly Hanstone</i>	DATE CONSIDERED <i>6/23/03</i>
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)	ATTY. DOCKET NO. 43876-128	SERIAL NO. 09/922,319
	APPLICANT HANSEN, et al.	
	FILING DATE August 02, 2001	GROUP 2671

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
UK	5,408,581	04/18/95	Suzuki, et al.			
	5,423,051	06/06/95	Fuller, et al.			
	5,426,600	06/20/95	Nakagawa, et al.			
	5,500,811	03/19/96	Corry			
	5,557,724	09/17/96	Sampat, et al.			
	5,588,152	12/24/96	Dapp, et al.			
	5,592,405	01/07/97	Gove, et al.			
	5,640,543	06/17/97	Farrell, et al.			
	5,642,306	06/24/97	Mennemeier, et al.			
	5,666,298	09/09/97	Peleg, et al.			
	5,669,010	09/16/97	Duluk, Jr.			
	5,673,407	09/30/97	Poland, et al.			
UK	5,675,526	10/07/97	Peleg, et al.			
UK	5,680,338	10/21/97	Agarwal, et al.			

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EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
UK	0 474 246 A2	06/09/91	Japan				
UK	0 654 733 A1	05/07/94	Germany				

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UK	The Visual Instruction Set (VIS) in UltraSPAR TM , L. Kohn, G. Maturana, M. Tremblay, A. Prabhu, G. Zyner, May 3, 1995.
UK	Osborne McGraw-Hill, i860 TM Microprocessor Architecture, Neal Margulis, Foreword by Les Kohn
UK	A General-Purpose Array Processor for Seismic Processing, Nov-Dec., 1984, Volume 1, No. 3) Revisiting past digital signal processor technology, Don Shaver- Jan-Mar. 1998.
UK	Accelerating Multimedia with Enhanced Microprocessors, Ruby B. Lee, 1995.

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MM	5,721,892	02/24/98	Peleg, et al.			
MM	5,734,874	03/31/98	Van Hook, et al.			
	5,757,432	05/26/98	Dulong, et al.			
	5,758,176	05/26/98	Agarwal, et al.			
	5,802,336	09/01/98	Peleg, et al.			
	5,809,292	09/15/98	Wilkinson, et al.			
	5,818,739	10/06/98	Peleg, et al.			
	5,825,677	10/20/98	Agarwal, et al.			
	5,835,782	11/10/98	Chu Lin, et al.			
	5,886,732	03/23/99	Humpleman			
	5,922,066	07/13/99	Cho, et al.			
	5,983,257	11/09/99	Dulong, et al.			
	6,016,538	01/18/00	Guttag, et al.			
MM	6,092,094	07/18/00	Ireton			
MM	6,401,194 B1	06/04/02	Nguyen, et al.			

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